Bandpass Filter

BFCN-4100+

 50Ω 3700 to 4500 MHz

The Big Deal

- Flat group delay (±45 pS)
- Narrow band/fast roll-off in LTCC
- Good passband VSWR (1.2:1 typical)



CASE STYLE: FV1206

Product Overview

The BFCN-4100+ LTCC Bandpass Filter is constructed using multilayer ceramic technology to achieve miniature size and high repeatability of performance. Wrap-around terminations minimize variations in performance due to parasitics. Covering 4100 MHz ±400 MHz, these units offer low insertion loss and good rejection at the band reject edges.

Key Features

Feature	Advantages
Flat group delay (±45pS)	The model has flat group delay which ensures low distortion.
Sharp shape factor	Sharp shape factor helps in adjacent channel rejection and hence increased selectivity.
Good VSWR, 1.2:1 typical over passband	This provides well matched input and output ports.
Wrap around termination	Provides excellent solderability and easy visual inspection capability.
LTCC construction	Provides a rugged package that is well suited for tough environments including high humidity and high temperature extremes.
Small size, 0.12" x 0.6" x 0.4"	The surface mount package enables BFCN-4100+ to be used in compact designs.

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50Q 3700 to 4500 MHz

BFCN-4100+



CASE STYLE: FV1206

Features

- Small size, 0.12" x 0.06"
- Temperature stable
- · Hermetically sealed
- LTCC construction

Applications

- · Harmonic rejection
- Transmitters / receivers

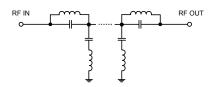
Parai	neter	F# Frequency (MHZ) Min.		Typ.	wax.	Unit	
	Center Frequency	_	_	_	4100	_	MHz
Pass Band	Insertion Loss	F1-F2	3700 - 4500	_	1.4	2.0	dB
	VSWR	F1-F2	3700 - 4500	_	1.5	2.0	:1
Cton Bond Lower	Insertion Loss	DC-F3	DC - 2200	20	25	_	dB
Stop Band, Lower	Stop Band, Lower VSWR		DC - 2200	_	25	_	:1
Stop Band, Upper	Insertion Loss	F4-F5	6000 - 9000	15	20	_	dB
Stop Baild, Opper	VSWR	F4-F5	6000 - 9000		20	_	:1

Electrical Specifications^{1,2} at 25°C

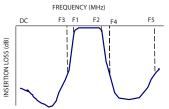
- 1. Measured on Mini-Circuits Characterization Test Board TB-270.
- 2. This filter is not intended for use as a DC Blocking circuit element. In Application where DC voltage is present at either input or output ports, blocking capacitors are required at the corresponding RF port.

Maximum Ratings				
-55°C to 100°C				
-55°C to 100°C				
1.5W max.				

Functional Schematic



Typical Frequency Response





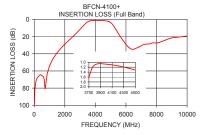
+RoHS Compliant

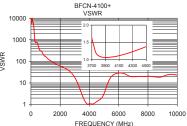
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

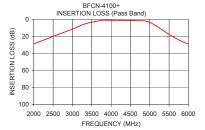


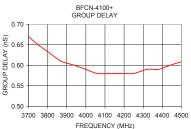
Typical Performance Data at 25°C

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)	Frequency (MHz)	Group Delay (nsec)
10.00	103.46	20111.00	3700.00	0.67
60.00	79.31	5792.02	3750.00	0.65
100.00	73.31	8202.75	3810.00	0.63
320.00	65.61	729.17	3870.00	0.61
1000.00	54.23	170.92	4000.00	0.59
1510.00	38.59	88.11	4060.00	0.58
2040.00	28.17	59.32	4130.00	0.58
3020.00	11.07	16.83	4190.00	0.58
3310.00	5.55	6.15	4250.00	0.58
3750.00	1.26	1.25	4320.00	0.59
4000.00	1.08	1.08	4380.00	0.59
4510.00	1.33	1.39	4440.00	0.60
4700.00	1.66	1.69	4510.00	0.61
5010.00	3.71	3.38		
6000.00	28.93	28.70		
7080.00	29.82	19.52		
8110.00	27.11	20.57		
9000.00	21.43	19.72		
10000.00	19.39	22.84		









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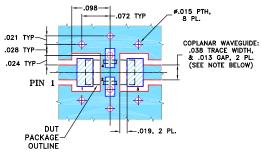
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Pin Connections

RF IN	1
RF OUT	3
GROUND	2,4

Demo Board MCL P/N: TB-270 Suggested PCB Layout (PL-137)



NOTES: 1. COPLANAR WAYEGUIDE PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH THICKNESS .020" ± .0015".
COPPER: 1/2 0Z. EACH SIDE.
FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.

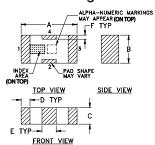
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

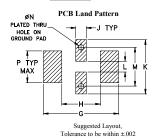
DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

Product Marking: AZ

Outline Drawing





Outline Dimensions (inch)

	G	F	E	D	С	В	Α
	.169	.009	.032	.020	.037	.063	.126
	4.29	0.23	0.81	0.51	0.94	1.60	3.20
wt	Р	N	M	L	K	J	Н
grams	.071	.012	.087	.024	.122	.024	.087
020	1.80	0.30	2 21	0.61	3 10	0.61	2 21

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